EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	9034	((370/389,395.7,395.72,412,428, 429) or (711/100)).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:37
L3	0	(stor$4 with packet with header) with (transmi$5 near3 count$3 near5 compar$5)	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:32
L4	0	(stor$4 with packet with header) with (transmi$5 near3 count$3 near5 compar$5).clm.	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:32
L5	12	(stor$4 with packet with header) and (transmi$5 near3 count$3 near5 compar$5)	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:33
L6	23	(stor$4 and packet and header) and (transmi$5 near3 count$3 near5 compar$5).clm.	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:32
L7	8882	((370/389,395.72,412,428,429) or (711/100)).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:33
L8	59	(packet with header) and (transmi$5 near3 count$3 near5 compar$5)	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:33
L9	12	8 and 2	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:33
L10	12	8 and 7	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:37
L11	9949	((370/389,390,395.7,395.72,412, 428,429) or (711/100)).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:37
L13	13	8 and 11	US-PGPUB; USPAT	OR	OFF	2007/02/02 14:37

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CHEN
First Name = EDMUND

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09087345	6148350	150	05/29/1998	SYSTEM FOR ALLOCATING AN INTEGER UNIT OF MEMORY GREATER THAN A REQUESTED SIZE AND FILLING THE EXTRA SPACE DUE TO DIFFERENCE IN SIZES WITH EXTRANEOUS DATA	CHEN, EDMUND
09087347	6055580	150	05/29/1998	SYSTEM FOR TRANSFERRING LENGTH ROUND DOWN TO CACHE LINE MULTIPLE CAPABLE TO DETERMINE TYPE OF READ REQUEST WHICH SELECTS AND READS PORTION OF PREFETCHED DATA IN MEMORY	CHEN, EDMUND
09087484	6266732	150	05/29/1998	INTERRUPT EVENTS CHAINING	CHEN, EDMUND
09237995	6351785	150	01/26/1999	INTERRUPT OPTIMIZATION USING VARYING QUANTITY	CHEN, EDMUND

				THRESHOLD	
09238651	6529986	150	01/26/1999	INTERRUPT OPTIMIZATION USING STORAGE TIME FOR PERIPHERAL COMPONENT EVENTS	CHEN, EDMUND
09238652	6574694	150	01/26/1999	INTERRUPT OPTIMIZATION USING TIME BETWEEN SUCCEEDING PERIPHERAL COMPONENT EVENTS	CHEN, EDMUND
09258500	6189066	150	02/26/1999	SYSTEM AND METHOD FOR DYNAMICALLY SELECTING INTERRUPT TIME INTERVAL THRESHOLD PARAMETERS	CHEN, EDMUND
09258501	6192440	150	02/26/1999	SYSTEM AND METHOD FOR DYNAMICALLY SELECTING INTERRUPT STORAGE TIME THRESHOLD PARAMETERS	CHEN, EDMUND
09258514	6189067	150	02/26/1999	SYSTEM AND METHOD FOR DYNAMICALLY SELECTING INTERRUPT QUANTITY THRESHOLD PARAMETERS	CHEN, EDMUND
09282311	6442617	150	03/31/1999	METHOD AND SYSTEM FOR FILTERING MULTICAST	CHEN, EDMUND

				PACKETS IN A PERIPHERAL COMPONENT ENVIRONMENT	
10021152	7007095	150	12/07/2001	METHOD AND APPARATUS FOR UNSCHEDULED FLOW CONTROL IN PACKET FORM	CHEN, EDMUND G.
10032729	Not Issued	80	12/28/2001	Method and apparatus for replicating packet data within a network element	CHEN, EDMUND G.
10193504	Not Issued	41	07/10/2002	Method and apparatus for out-of-order processing of packets	CHEN, EDMUND G.
10299236	Not Issued	41	11/18/2002	Method and apparatus for implementing a switching unit including a bypass path	CHEN, EDMUND G.
10327555	Not Issued	41	12/20/2002	Method and apparatus for out-of-order processing of packets using linked lists	CHEN, EDMUND G.
60345416	Not Issued	159	12/31/2001	Method and apparatus for out-of-order processing of packets	CHEN, EDMUND G.
60358079	Not Issued	159	02/19/2002	Method and apparatus for implementing a switching unit including a bypass path	CHEN, EDMUND G.
60412433	Not Issued	159	09/20/2002	Method and apparatus for out-of-order processing of packets using linked	CHEN, EDMUND G.

				lists	
60309580	Not Issued	159	08/02/2001	Method and apparatus for classifying network data	CHEN, EDMUND G.
06080284	4293848	150	10/01/1979	MOS ANALOG-TO-DIGITAL CONVERTER	CHENG, EDMUND K.

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = ROZARIO
First Name = RANJIT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09328940	6345328	150	06/09/1999	GEAR BOX FOR MULTIPLE CLOCK DOMAINS	ROZARIO, RANJIT
10032729	Not Issued	80	12/28/2001	Method and apparatus for replicating packet data within a network element	ROZARIO, RANJIT
10291999	Not Issued	41	11/12/2002	Updating of routing data in a network element	ROZARIO, RANJIT
10292000	Not Issued	71	11/12/2002	Method and apparatus for accessing routing data from memory	ROZARIO, RANJIT
60309556	Not Issued	159	08/02/2001	Method and apparatus for classifying network data	ROZARIO, RANJIT
60344122	Not Issued	159	12/28/2001	Method and apparatus for accessing routing data from memory	ROZARIO, RANJIT
60368200	Not Issued	159	03/28/2002	Updating of routing data in a network element	ROZARIO, RANJIT
09740669	Not Issued	164	12/18/2000	SCHEDULER FOR A DIRECT MEMORY	ROZARIO, RANJIT J.

				ACCESS DEVICE HAVING MULTIPLE CHANNELS	
09740670	6618793	150	12/18/2000	FREE MEMORY MANAGER SCHEME AND CACHE	ROZARIO, RANJIT J.
10650317	6961822	150	08/27/2003	FREE MEMORY MANAGER SCHEME AND CACHE	ROZARIO, RANJIT J.
08340132	5745732	150	11/15/1994	COMPUTER SYSTEM INCLUDING SYSTEM CONTROLLER WITH A WRITE BUFFER AND PLURAL READ BUFFERS FOR DECOUPLED BUSSES	ROZARIO, RANJIT J.
09151861	6173378	150	09/11/1998	IMPLEMENTATION OF THE AGP REQUEST QUEUES USING FIFOS	ROZARIO, RANJIT J.
09151862	6253262	150	09/11/1998	ARBITRATING FIFO IMPLEMENTATION WHICH POSITIONS INPUT REQUEST IN A BUFFER ACCORDING TO ITS STATUS	ROZARIO, RANJIT J.

Inventor Search Completed: No Records to Display.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CHERUKURI
First Name = RAVIKRISHNA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09134171	6223198	150	08/14/1998	METHOD AND APPARATUS FOR MULTI-FUNCTION ARITHMETIC	CHERUKURI, RAVIKRISHNA
09151861	6173378	150	09/11/1998	IMPLEMENTATION OF THE AGP REQUEST QUEUES USING FIFOS	CHERUKURI, RAVIKRISHNA
09151862	6253262	150	09/11/1998	ARBITRATING FIFO IMPLEMENTATION WHICH POSITIONS INPUT REQUEST IN A BUFFER ACCORDING TO ITS STATUS	CHERUKURI, RAVIKRISHNA
09328940	6345328	150	06/09/1999	GEAR BOX FOR MULTIPLE CLOCK DOMAINS	CHERUKURI, RAVIKRISHNA
09740184	7020736	150	12/18/2000	METHOD AND APPARATUS FOR SHARING MEMORY SPACE ACROSS MUTLIPLE PROCESSING UNITS	CHERUKURI, RAVIKRISHNA
09740669	Not Issued	164	12/18/2000	SCHEDULER FOR A DIRECT MEMORY ACCESS DEVICE HAVING MULTIPLE	CHERUKURI, RAVIKRISHNA

				CHANNELS	
09740670	6618793	150	12/18/2000	FREE MEMORY MANAGER SCHEME AND CACHE	CHERUKURI, RAVIKRISHNA
09782474	6381625	150	02/12/2001	METHOD AND APPARATUS FOR CALCULATING A POWER OF AN OPERAND	CHERUKURI, RAVIKRISHNA
09782475	6397238	150	02/12/2001	METHOD AND APPARATUS FOR ROUNDING IN A MULTIPLIER ARITHMETIC	CHERUKURI, RAVIKRISHNA
10021152	7007095	150	12/07/2001	METHOD AND APPARATUS FOR UNSCHEDULED FLOW CONTROL IN PACKET FORM	CHERUKURI, RAVIKRISHNA
10032729	Not Issued	80	12/28/2001	Method and apparatus for replicating packet data within a network element	CHERUKURI, RAVIKRISHNA
10650317	6961822	150	08/27/2003	FREE MEMORY MANAGER SCHEME AND CACHE	CHERUKURI, RAVIKRISHNA
60027329	Not Issued	159	09/30/1996	X86 MICROPROCESSOR WITH MULTI-MEDIA EXTENSIONS	CHERUKURI, RAVIKRISHNA
60309556	Not Issued	159	08/02/2001	Method and apparatus for classifying network data	CHERUKURI, RAVIKRISHNA
60309580	Not Issued	159	08/02/2001	Method and apparatus for classifying network data	CHERUKURI, RAVIKRISHNA
09872936	6976096	150	06/02/2001	METHOD AND APPARATUS FOR CONTROLLING THE	CHERUKURI, RAVIKRISHNA V.

				ADMISSION OF DATA INTO A NETWORK ELEMENT	
11260775	Not Issued	30	10/26/2005	Method and apparatus for controlling the admission of data into a network element	CHERUKURI, RAVIKRISHNA V.
08340132	5745732	150	11/15/1994	COMPUTER SYSTEM INCLUDING SYSTEM CONTROLLER WITH A WRITE BUFFER AND PLURAL READ BUFFERS FOR DECOUPLED BUSSES	CHERUKURI, RAVIKRISHNA V.
08970042	6006307	250	11/13/1997	COMPUTER SYSTEM EMPLOYING A MIRRORED MEMORY SYSTEM FOR PROVIDING PREFETCH BANDWIDTH	CHERUKURI, RAVIKRISHNA V.

Inventor Search Completed: No Records to Display.

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